

Docket No.: 08211/0200347-US0
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:
Ols Hidri et al.

Patent No.: 7,071,670

Issued: July 4, 2006

For: GENERATING REFERENCE VOLTAGES

**REQUEST FOR CERTIFICATE OF CORRECTION
PURSUANT TO 37 CFR 1.323 AND PATENT OFFICE MISTAKE (37 CFR 1.322)**

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Upon reviewing the above-identified patent, Patentee noted several errors which should be corrected.

In the Claims:

Column 8, Line 13, In Claim 12, after "element" insert -- , --.

Column 8, Line 41, In Claim 18, delete "fist" and insert -- first --.

Enclosed please find marked up copies of pages 4 & 5 of the claims.

The errors were found in the application as filed by applicant. The errors now sought to be corrected are inadvertent typographical errors. The correction of which does not involve new matter or require reexamination.

First Page Col. 2 (Attorney, Agent or Firm), Line 2, Delete "PC" and insert -- P.C. --.

Sheet 6 or 7 (Beside Box 620), Line 1, Delete "VDD" and insert -- V_{DD} - -.

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

The Commissioner is authorized to charge any deficiency of up to \$300.00 or credit any excess in this fee to Deposit Account No. 04-0100. Payment of \$100.00 is enclosed herewith.

Dated: September²⁰, 2006

Respectfully submitted,

By 
Flynn Barrison

Registration No.: 53,970
DARBY & DARBY P.C.
P.O. Box 5257
New York, New York 10150-5257
(212) 527-7700
(212) 527-7701 (Fax)
Attorneys/Agents For Applicant

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

Page 1 of 1

PATENT NO. : 7,071,670
APPLICATION NO. : 10/695,921
ISSUE DATE : July 4, 2006
INVENTOR(S) : Ols Hidri et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Application:

First Page Col. 2 (Attorney, Agent or Firm), Line 2, Delete "PC" and

insert -- P.C. --.

Sheet 6 or 7 (Beside Box 620), Line 1, Delete "VDD" and insert -- V_{DD} --.

In the Claims:

Column 8, Line 13, In Claim 12, after "element" insert -- , --.

Column 8, Line 41, In Claim 18, delete "fist" and insert -- first --.

MAILING ADDRESS OF SENDER:
Flynn Barrison
DARBY & DARBY P.C.
P.O. Box 5257
New York, New York 10150-5257

Application No. 10/695,921

Docket No.: 08211/0200347-US0

11. (Original) The device of claim 10, wherein
the first branch current has a different manufacturing process variation
dependence than the second branch current.
12. (Original) The device of claim 10, wherein
the second branch current is larger than the first branch current.
13. (Currently Amended) The device of claim 10, further comprising:
~~means for~~ a fourth circuit that enables the draining from the intermediate node
a drained current that approximately equals the first branch current, and has
approximately the same manufacturing process variation dependence as the first
branch current.
14. (Currently Amended) A method comprising:
forcing a first branch current through a resistive element to generate a resistive voltage
drop;
forcing a second branch current through a junction device that includes a junction and has
a negative temperature coefficient to generate a junction voltage drop, wherein the second branch
current is different from the first branch current; [[and]]
adding the resistive voltage drop to the junction voltage drop to generate a
reference voltage;
wherein the first branch current is arranged to transmit a first bias current through the
resistive element, and wherein the second branch current is arranged to transmit a second bias
current through the junction device for biasing the junction, without transmitting the second bias
current through the resistive element, and wherein the first bias current reaches an intermediate
node after the resistive element and before the junction device; and
extracting a drained current from the intermediate node with a third branch current.

Application No. 10/695,921

Docket No.: 08211/0200347-US0

15. (Original) The method of claim 14, wherein
the first branch current has a different manufacturing process variation
dependence than the second branch current.
16. (Original) The method of claim 14, wherein
the second branch current is larger than the first branch current.
17. (Original) The method of claim 14, further comprising:
combining the first branch current with a bias current to generate the second branch
current.
18. (Original) The method of claim 17, further comprising:
controlling the bias current by the reference voltage.
19. (Original) The method of claim 17, further comprising:
draining at least some of the first branch current.
20. (Original) The method of claim 19, wherein
the drained current approximately equals the first branch current, and has approximately
the same manufacturing process variation dependence as the first branch current.